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APPLICANTS

Masaru Takaishi, Kyoto, JAPAN;

** CONTINUING DATA ***** *None A/c*** FOREIGN APPLICATIONS ***** *Yes A/c*
JAPAN 2003-95384 03/31/2003

IF REQUIRED, FOREIGN FILING LICENSE GRANTED **

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| Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no | 35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance | STATE OR COUNTRY JAPAN | SHEETS DRAWING 4 | TOTAL CLAIMS 13 | INDEPENDENT CLAIMS 3 |
| Verified and Acknowledged <i>Asst Examiner Sarker</i> Examiner's Signature Initials | | | | | |

ADDRESS

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TITLE

Semiconductor device with low resistance region

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